| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 2074 | ((level adj1 shift$4) or (level adj1 conver$4) or (level adj1 translat$4)) and (cross-coupl$3 or (cross adj1 coupl$4)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/07 16:31 |
| L2 | 358 | 1 and ((differential adj1 input) or (complement$4 adj1 input)) and ((differential adj1 output) or (complement$4 adj1 output)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/07 16:33 |
| L3 | 169 | 2 and ((n-type NEAR1 transistor) or nmos or nfet) and ((p-type NEAR1 transistor) or pmos or pfet) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/07 16:35 |

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 1 | (((level adj1 shift$4) or (level adj1 conver$4) or (level adj1 translat$4)) and ((n-type NEAR1 transistor) or nmos or nfet or mosfet) and (p-type adj1 transistor or pmos or pfet) and (data adj1 input) and (control adj1 input) and (cross-coupl$4 or (cross adj1 coupl$4))).clm. | US-PGPUB | OR | OFF | 2005/10/07 16:51 |
| L2 | 1 | (((level adj1 shift$4) or (level adj1 conver$4) or (level adj1 translat$4)) and ((n-type NEAR1 transistor) or nmos or nfet or mosfet) and (p-type adj1 transistor or pmos or pfet) and input and (control adj1 input) and (cross-coupl$4 or (cross adj1 coupl$4))).clm. | US-PGPUB | OR | OFF | 2005/10/07 16:51 |
| L3 | 7 | (((level adj1 shift$4) or (level adj1 conver$4) or (level adj1 translat$4)) and ((n-type NEAR1 transistor) or nmos or nfet or mosfet) and (p-type adj1 transistor or pmos or pfet) and input and control and (cross-coupl$4 or (cross adj1 coupl$4))).clm. | US-PGPUB | OR | OFF | 2005/10/07 16:52 |